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FIG. 1A

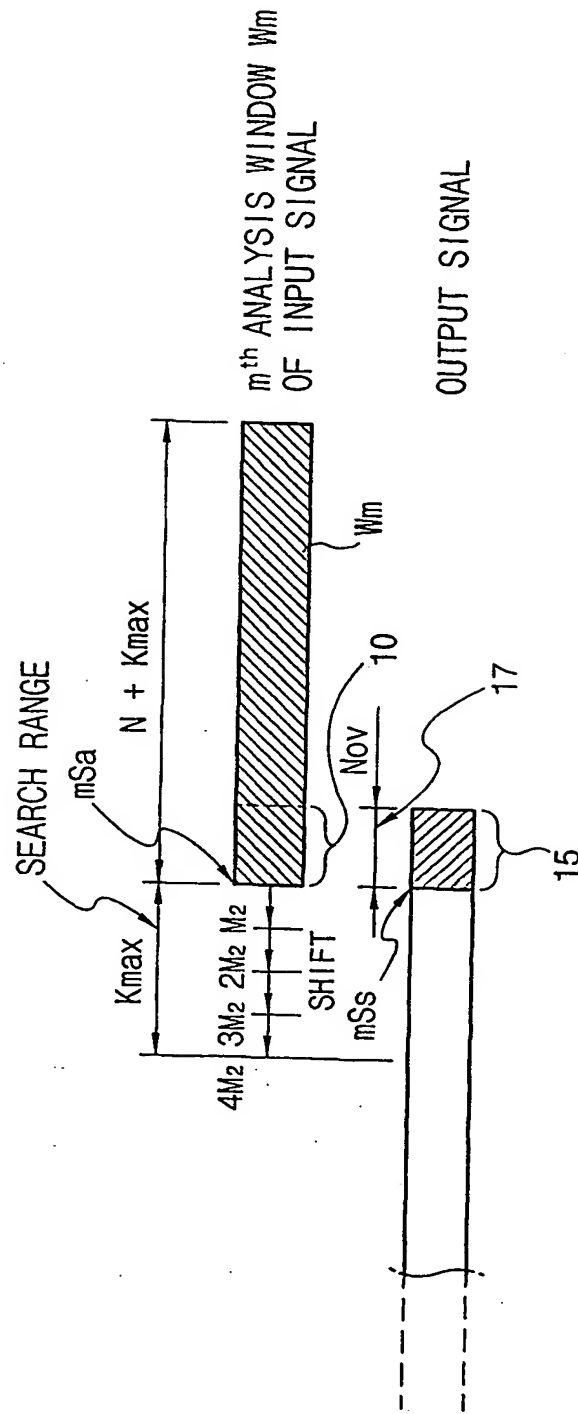
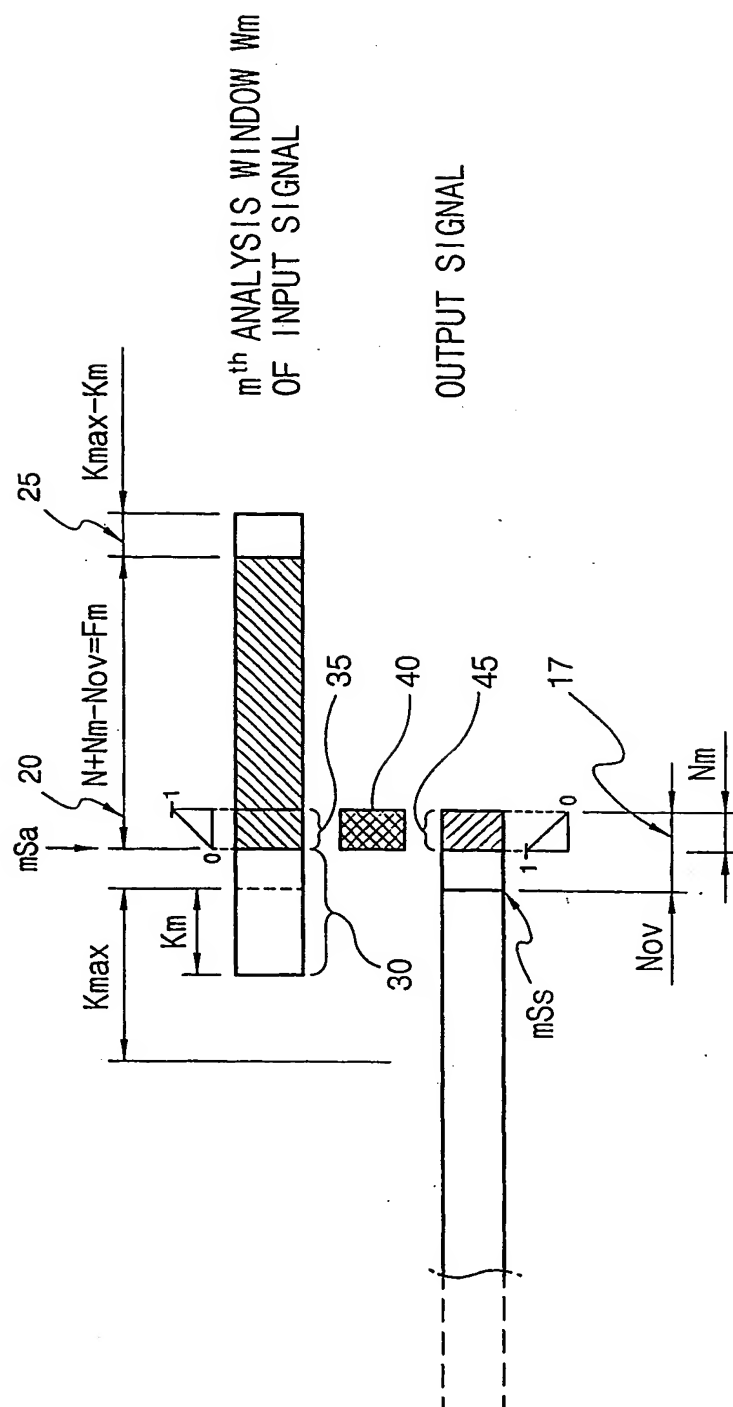
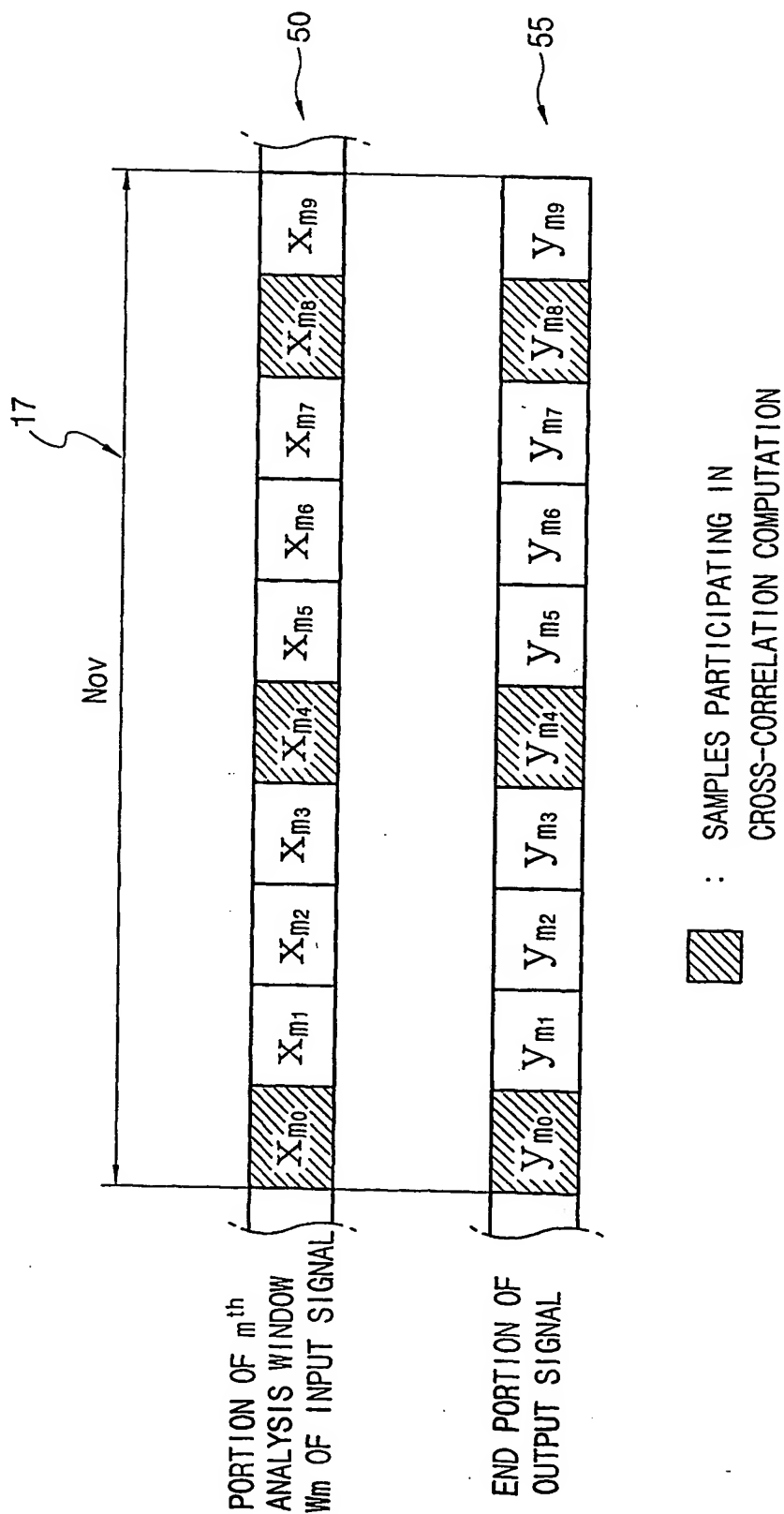


FIG. 1B



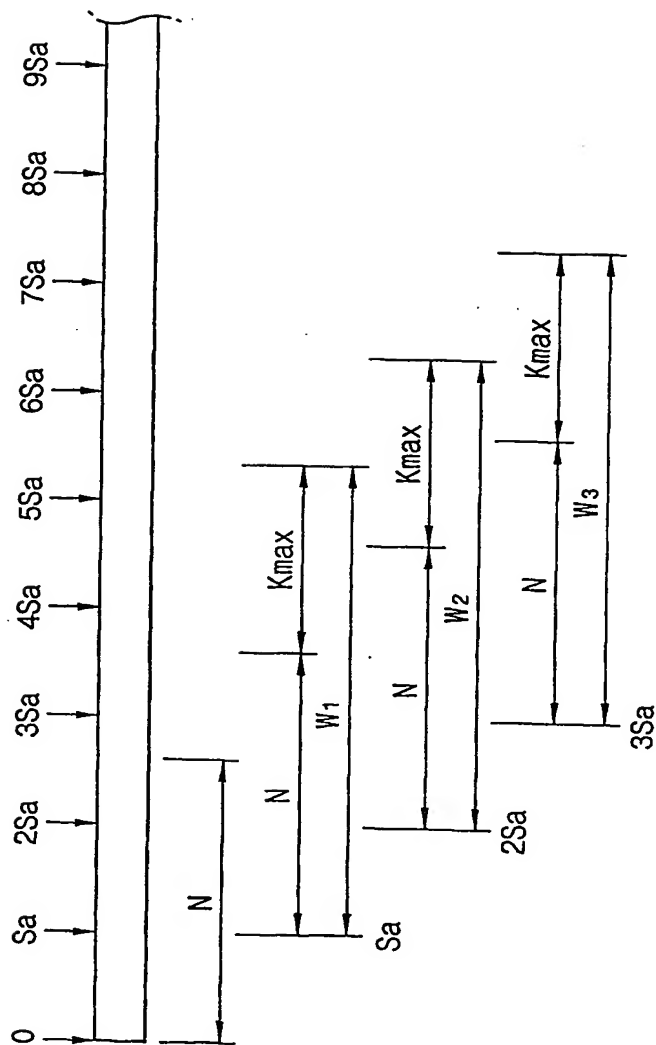
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FIG. 2



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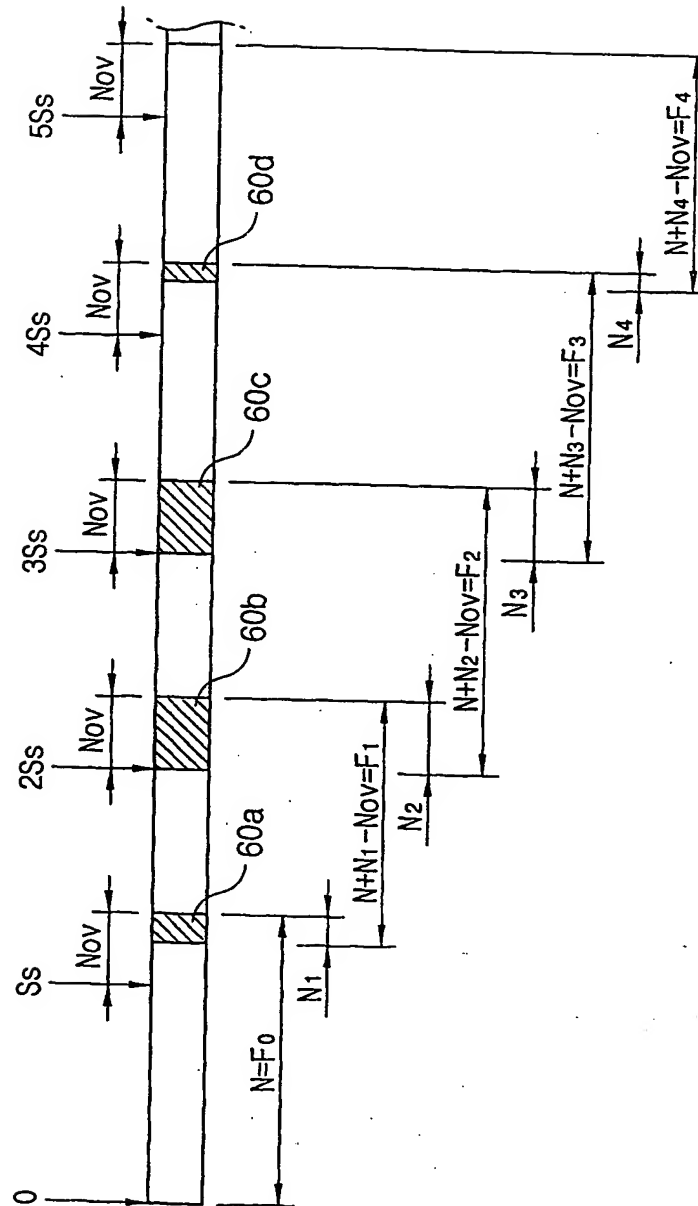
FIG. 3A

INPUT SIGNAL
(WHEN $\alpha=2$)

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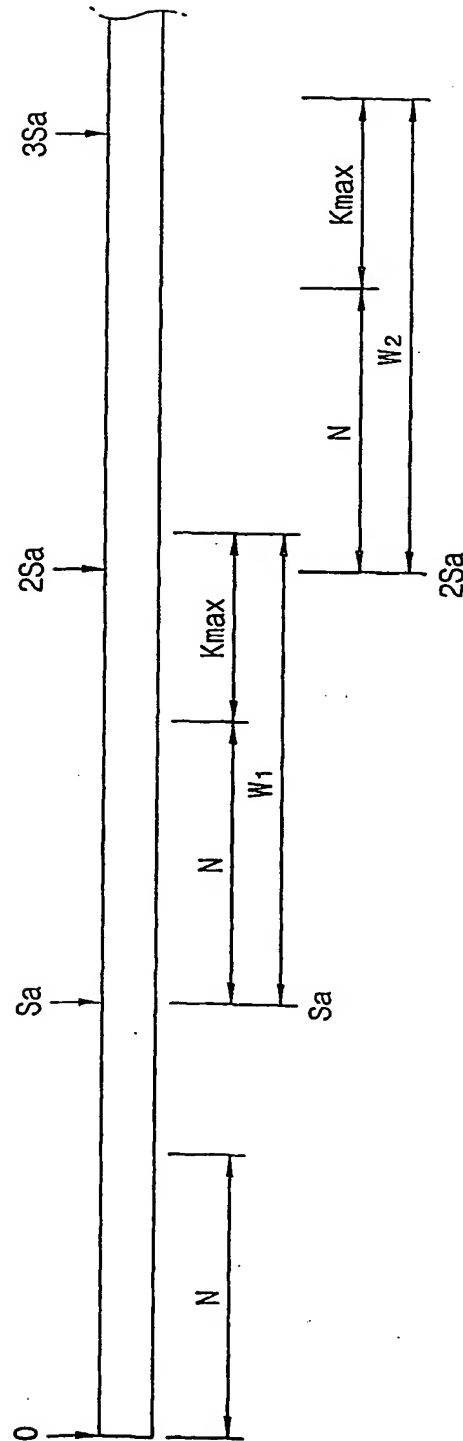
FIG. 3B

OUTPUT SIGNAL
(WHEN $\alpha = 2$)



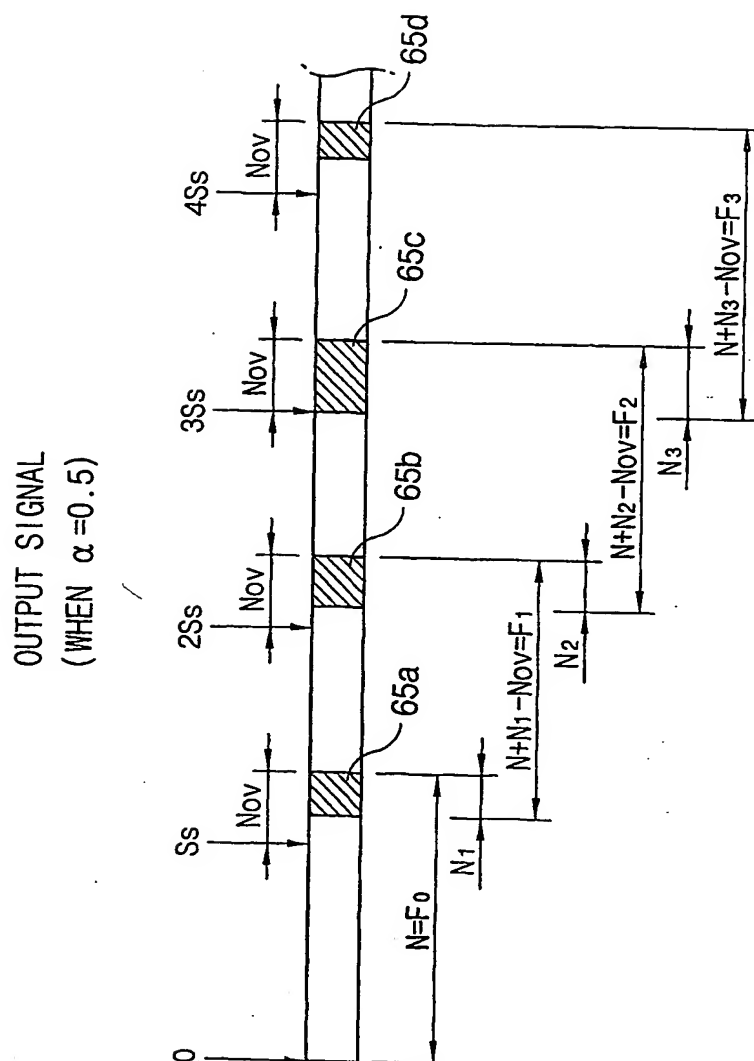
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FIG. 4A

INPUT SIGNAL
(WHEN $\alpha = 0.5$)

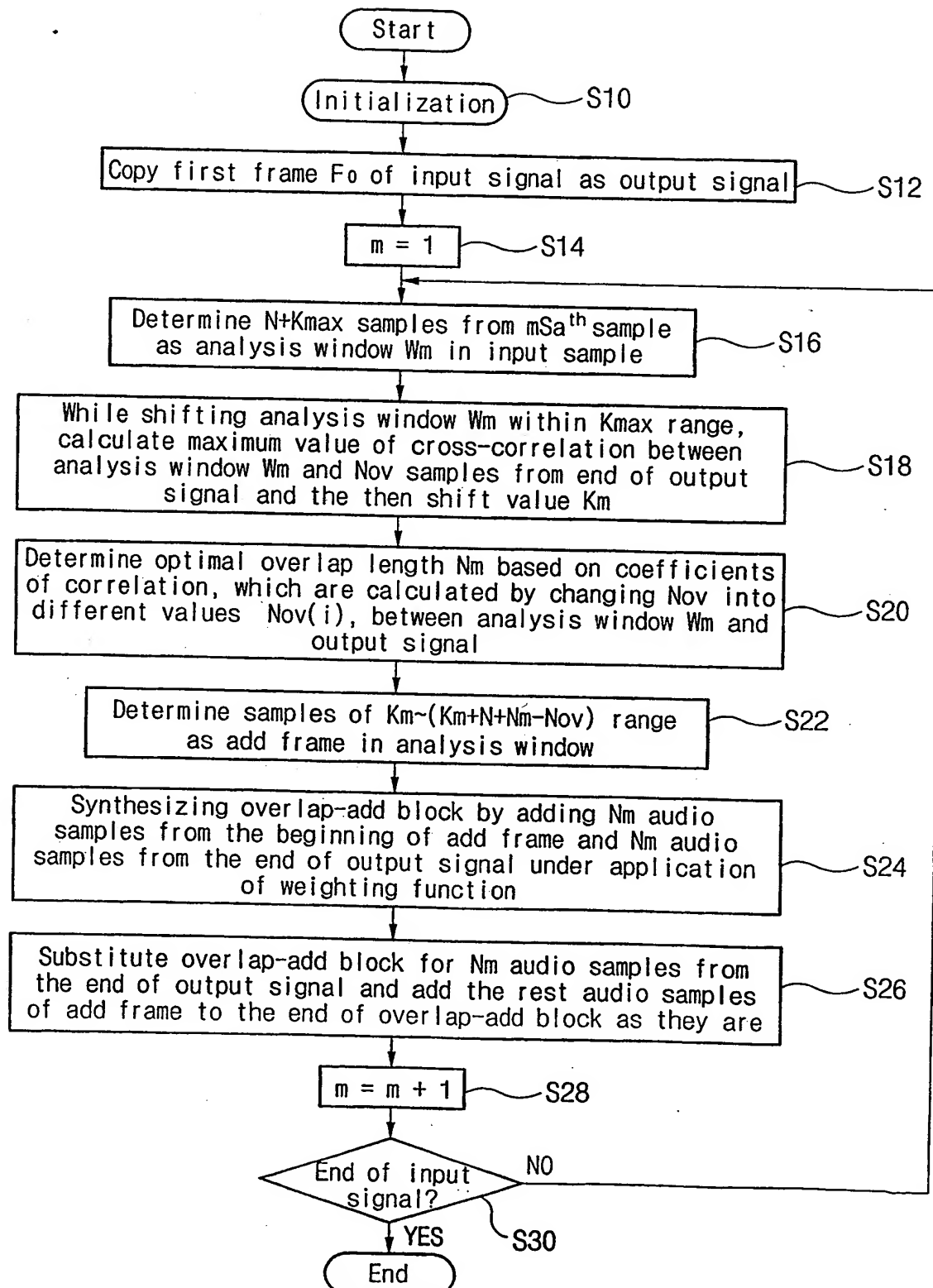
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FIG. 4B



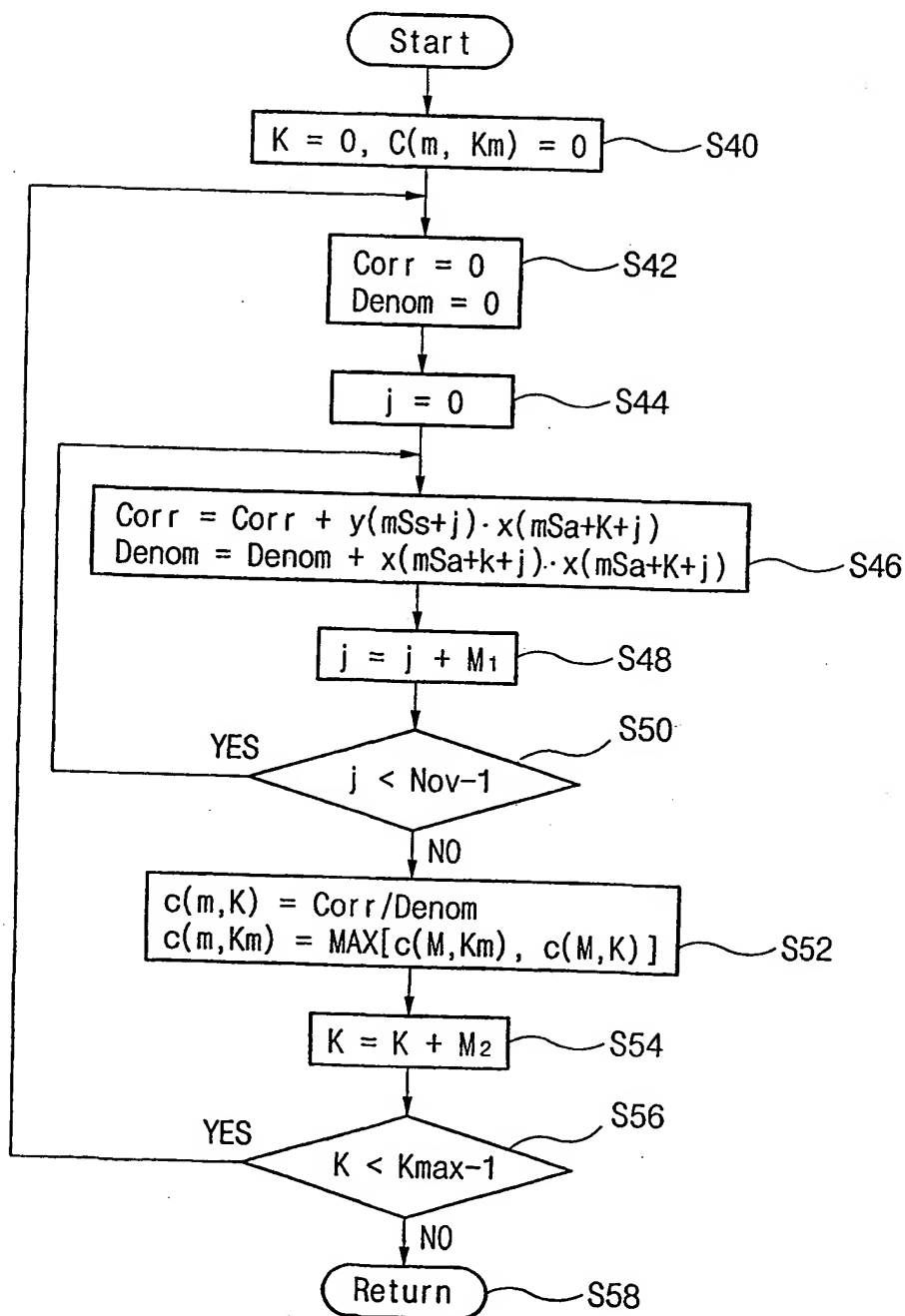
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FIG. 5



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FIG. 6

DETAILED PROCEDURES OF STEP 18

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FIG. 7

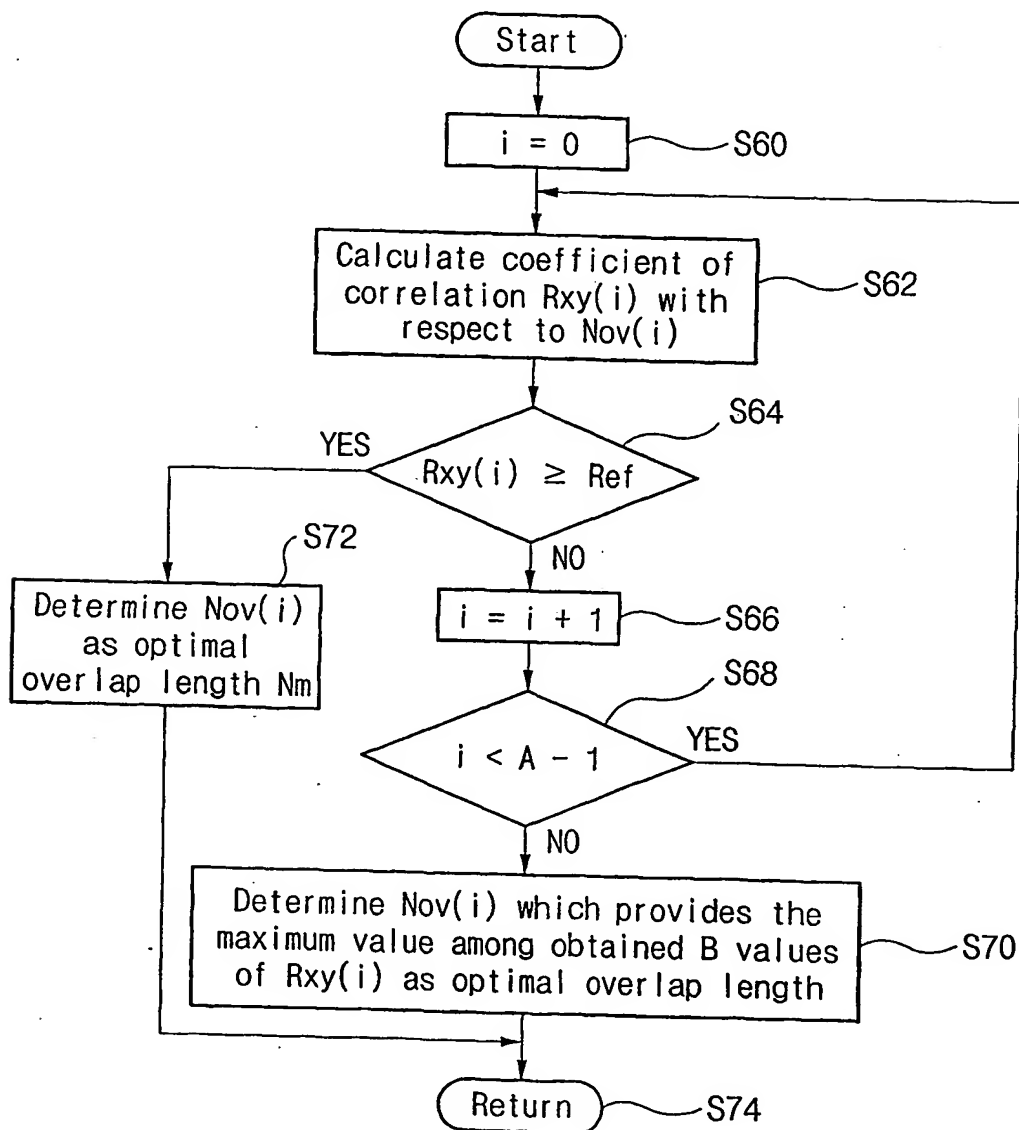
DETAILED PROCEDURES OF STEP 20

FIG. 8

